



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richmond et al.

Assignee: ZiLOG, Inc.

Title: "Clock Controller with Clock Source Fail-Safe Logic"

Appl. No.: 10/764,391

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Examiner: James F. Sugent

Art Unit: 2116

Docket No.: ZIL-555

March 27, 2007

Mail Stop RCE  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CONTINUED EXAMINATION (RCE)**

Dear Sir:

This is a Request for Continued Examination (RCE) under 37 C.F.R. §1.114 of the above-identified application. Please consider the Amendment that is being filed herewith. A check is enclosed that includes the amount of \$790.00 for the RCE fee required under 37 C.F.R. §1.17(e).

Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By Darien K. Wallace  
Darien K. Wallace

Date of Deposit: March 27, 2007

Respectfully submitted,

Darien K. Wallace

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